

## Digital Integrated Circuits 2nd Edition

Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work. The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of Digital Integrated Circuits: Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition.

This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

This book describes the design of CMOS circuits for ultra-low power consumption including analog, radio frequency (RF), and digital signal processing circuits (DSP). The book addresses issues from circuit and system design to production design, and applies the ultra-low power circuits described to systems for digital hearing aids and capsule endoscope devices. Provides a valuable introduction to ultra-low power circuit design, aimed at practicing design engineers; Describes all key building blocks of ultra-low power circuits, from a systems perspective; Applies circuits and systems described to real product examples such as hearing aids and capsule endoscopes.

"The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. This book deals with the design of high-speed integrated circuits for optical communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery circuits, and multiplexers. This second edition of this best selling textbook has been updated to provide information on the latest developments in the field"--

CMOS Digital Integrated Circuits

The Design of CMOS Radio-Frequency Integrated Circuits

Design of Integrated Circuits for Optical Communications

Foundations of Analog and Digital Electronic Circuits

A First Course

The development of large-scale integrated systems on a chip has had a dramatic effect on circuit design methodology. Recent years have seen an escalation of interest in systems level integration (system-on-a-chip) and the development of low power, high chip density circuits and systems. Kurt Hoffmann sets out to address a wide range of issues relating to the design and integration of integrated circuit components and provides readers with the methodology by which simple equations for the estimation of transistor geometries and circuit behaviour can be deduced. The broad coverage of this unique book ranges from field effect transistor design, MOS transistor modelling and the fundamentals of digital CMOS circuit design through to MOS memory architecture and design. Highlights the increasing requirement for information on system-on-a-chip design and integration. Combines coverage of semiconductor physics, digital VLSI design and analog integrated circuits in one volume for the first time. Written with the aim of bridging the gap between semiconductor device physics and practical circuit design. Introduces the basic behaviour of semiconductor components for ICs and covers the design of both digital and analog circuits in CMOS and BiCMOS technologies. Broad coverage will appeal to both students and practising engineers alike. Written by a respected expert in the field with a proven track record of publications in this field. Drawing upon considerable experience within both industry and academia, Hoffmann's outstanding text, will prove an invaluable resource for designers, practising engineers in the semiconductor device field and electronics systems industry as well as Postgraduate students of microelectronics, electrical and computer engineering.

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed. Unlike books currently on the market, this book attempts to satisfy two goals: combine circuits and electronics into a single, unified treatment, and establish a strong connection with the contemporary world of digital systems. It will introduce a new way of looking not only at the treatment of circuits, but also at the treatment of introductory coursework in engineering in general. Using the concept of "abstraction," the book attempts to form a bridge between the world of physics and the world of large computer systems. In particular, it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems. Computer systems are simply one type of electrical systems. +Balances circuits theory with practical digital electronics applications. +Illustrates concepts with real devices. +Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach. +Written by two educators well known for their innovative teaching and research and their collaboration with industry. +Focuses on contemporary MOS technology.

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of

device structures and process limitations affecting circuit performance.

Analog Integrated Circuit Design

System Integration

Circuit Design, Layout, and Simulation

Analysis and Design

Three-dimensional Integrated Circuit Design

*Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning*

*Equips students with essential industry-relevant knowledge through in-depth explanations, practical applications, examples, and exercises.*

*Examines all important aspects of integrated circuit design, fabrication, assembly and test processes as they relate to quality and reliability. This second edition discusses in detail: the latest circuit design technology trends; the sources of error in wafer fabrication and assembly; avenues of contamination; new IC packaging methods; new in-line process monitors and test structures; and more.; This work should be useful to electrical and electronics, quality and reliability, and industrial engineers; computer scientists; integrated circuit manufacturers; and upper-level undergraduate, graduate and continuing-education students in these disciplines.*

*Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.*

*Enabling the Internet of Things*

*Radio Frequency Integrated Circuits and Systems*

*Design of Analog Integrated Circuits and Systems*

*Digital Integrated Circuits*

*Programming Challenges*

This undergraduate textbook for electrical and computer engineering students is dedicated solely to digital CMOS electronics. It covers many of the topics of graduate level textbooks, but in an introductory style specifically crafted (and course tested) for undergraduates. Students will not need a prerequisite in analog electronics, allowing instructors flexibility in course scheduling. This book blends the academic and industrial experience of the authors to define a base of electronics instruction for the CMOS chip industry. CMOS Digital Integrated Circuits: A First Course teaches the fundamentals of modern CMOS technology by focusing on central themes and avoiding excessive details. Extensive examples, self-exercises, and end-of chapter problems assist in teaching the current practices of industry and subjects taught by graduate courses in microelectronics. Computer engineering curriculums can remove the analog electronics prerequisite altogether when adopting this book. Key Features CMOS technology written specifically for (and tested by) undergraduates. Equal treatment to both types of MOSFET transistors that make up computer circuits. Power properties of logic circuits. Physical and electrical properties of metals. Introduction of timing circuit electronics. Introduction of layout. Real-world examples and problem sets.

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

VERILOG HDL, Second Edition by Samir Palnitkar With a Foreword by Prabhu Goel Written for both experienced and new users, this book gives you broad coverage of VerilogHDL. The book stresses the practical design and verification perspective of Verilog rather than emphasizing only the language aspects. The information presented is fully compliant with the IEEE 1364-2001 Verilog HDL standard. Among its many features, this edition-  
 • Describes state-of-the-art verification methodologies  
 • Provides full coverage of gate, dataflow (RTL), behavioral and switch modeling  
 • Introduces you to the Programming Language Interface (PLI)  
 • Describes logic synthesis methodologies  
 • Explains timing and delay simulation  
 • Discusses user-defined primitives  
 • Offers many practical modeling tips  
 Includes over 300 illustrations, examples, and exercises, and a Verilog resource list. Learning objectives and summaries are provided for each chapter. About the CD-ROM The CD-ROM contains a Verilog simulator with a graphical user interface and the source code for the examples in the book. What people are saying about Verilog HDL- "Mr. Palnitkar illustrates how and why Verilog HDL is used to develop today's most complex digital designs. This book is valuable to both the novice and the experienced Verilog user. I highly

recommend it to anyone exploring Verilog-based design." -Rajeev Madhavan, Chairman and CEO, Magma Design Automation  
 "This book is unique in its breadth of information on Verilog and Verilog-related topics. It is fully compliant with the IEEE 1364-2001 standard, contains all the information that you need on the basics, and devotes several chapters to advanced topics such as verification, PLI, synthesis and modeling techniques." -Michael McNamara, Chair, IEEE 1364-2001 Verilog Standards Organization  
 "This has been my favorite Verilog book since I picked it up in college. It is the only book that covers practical Verilog. A must have for beginners and experts." -Berend Ozceri, Design Engineer, Cisco Systems, Inc. "Simple, logical and well-organized material with plenty of illustrations, makes this an ideal textbook." -Arun K. Somani, Jerry R. Junkins Chair Professor, Department of Electrical and Computer Engineering, Iowa State University, Ames  
 PRENTICE HALL Professional Technical Reference Upper Saddle River, NJ 07458 www.phptr.com ISBN: 0-13-044911-3

When it comes to electronics, demand grows as technology shrinks. From consumer and industrial markets to military and aerospace applications, the call is for more functionality in smaller and smaller devices. Culled from the second edition of the best-selling *Electronics Handbook, Microelectronics, Second Edition* presents a summary of the current state of microelectronics and its innovative directions. This book focuses on the materials, devices, and applications of microelectronics technology. It details the IC design process and VLSI circuits, including gate arrays, programmable logic devices and arrays, parasitic capacitance, and transmission line delays. Coverage ranges from thermal properties and semiconductor materials to MOSFETs, digital logic families, memory devices, microprocessors, digital-to-analog and analog-to-digital converters, digital filters, and multichip module technology. Expert contributors discuss applications in machine vision, ad hoc networks, printing technologies, and data and optical storage systems. The book also includes defining terms, references, and suggestions for further reading. This edition features two new sections on fundamental properties and semiconductor devices. With updated material and references in every chapter, *Microelectronics, Second Edition* is an essential reference for work with microelectronics, electronics, circuits, systems, semiconductors, logic design, and microprocessors.

CMOS VLSI Design : A circuits and systems perspective

High-Frequency Integrated Circuits

Low Power Design Methodologies

From VLSI Architectures to CMOS Fabrication

Digital VLSI Chip Design with Cadence and Synopsys CAD Tools

The 2nd Edition of *Analog Integrated Circuit Design* focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

*Low Power Design Methodologies* presents the first in-depth coverage of all the layers of the design hierarchy, ranging from the technology, circuit, logic and architectural levels, up to the system layer. The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction. Finally, it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments. The individual chapters are written by the leading researchers in the area, drawn from both industry and academia. Extensive references are included at the end of each chapter. Audience: A broad introduction for anyone interested in low power design. Can also be used as a text book for an advanced graduate class. A starting point for any aspiring researcher.

**KEY BENEFIT:** This hands-on book leads readers through the complete process of building a ready-to-fabricate CMOS integrated circuit using popular commercial design software. **KEY TOPICS:** The VLSI CAD flow described in this book uses tools from two vendors: Cadence Design Systems, Inc. and Synopsys Inc. Detailed tutorials include step-by-step instructions and screen shots of tool windows and dialog boxes. **MARKET:** A useful reference for chip designers.

Many interesting design trends are shown by the six papers on operational amplifiers (Op Amps). Firstly, there is the line of stand-alone Op Amps using a bipolar IC technology which combines high-frequency and high voltage. This line is represented in papers by Bill Gross and Derek Bowers. Bill Gross shows an improved high-frequency compensation technique of a high quality three stage Op Amp. Derek Bowers improves the gain and frequency behaviour of the stages of a two-stage Op Amp. Both papers also present trends in current-mode feedback Op Amps. Low-voltage bipolar Op Amp design is presented by Ieroen Fonderie. He shows how multipath nested Miller compensation can be applied to turn rail-to-rail input and output stages into high quality low-voltage Op Amps. Two papers on CMOS Op Amps by Michael Steyaert and Klaas Bult show how high speed and high gain VLSI building blocks can be realised. Without departing from a single-stage OT A structure with a folded cascode output, a thorough high frequency design technique and a gain-boosting technique contributed to the high-speed and the high-gain achieved with these Op Amps. Finally, Rinaldo Castello shows us how to provide output power with CMOS buffer amplifiers. The combination of class A and AB stages in a multipath nested Miller structure provides the required linearity and bandwidth.

Electronic Design Automation for IC System Design, Verification, and Testing

From Transistor Design to Large Scale Integrated Circuits

A Design Perspective

Design of Analog CMOS Integrated Circuits

Circuits, Systems, and Applications

*With vastly increased complexity and functionality in the "nanometer era" (i.e. hundreds of millions of transistors on one chip), increasing the performance of integrated circuits has become a challenging task. Connecting effectively (interconnect design) all of these chip elements has become the greatest determining factor in overall performance. 3-D integrated circuit design may offer the best solutions in the near future. This is the first book on 3-D integrated circuit design, covering all of the technological and design aspects of this emerging design paradigm, while proposing effective solutions to specific challenging problems concerning the design of 3-D integrated circuits. A handy, comprehensive reference or a practical design guide, this book provides a sound foundation for the design of 3-D integrated circuits. \* Demonstrates how to overcome "interconnect bottleneck" with 3-D integrated circuit design...leading edge design techniques offer solutions to problems (performance/power consumption/price) faced by all circuit designers \* The FIRST book on 3-D integrated circuit design...provides up-to-date information that is otherwise difficult to find \* Focuses on design issues key to the product development cycle...good design plays a major role in exploiting the implementation flexibilities offered in the 3-D \* Provides*

*broad coverage of 3-D integrated circuit design, including interconnect prediction models, thermal management techniques, and timing optimization...offers practical view of designing 3-D circuits*

*This book, first published in 2004, is an expanded and revised edition of Tom Lee's acclaimed RFIC text.*

*It follows with a thorough treatment of design operational and operational transconductance amplifiers, and concludes with a unified presentation of sample-data and continuous-time signal processing systems.*

*The fourth edition of the best-selling text details the modern techniques for the design of complex and high-performance CMOS systems on a chip. Covering the fundamentals of CMOS design from the digital systems level to the circuit level, this book explains the fundamental principles and is a guide to good design practices*

*Verilog HDL*

*From Integrated Circuits to Integrated Systems*

*Analysis And Design Of Digital Integrated Circuits, In Deep Submicron Technology (special Indian Edition)*

*Ultra-Low Power Integrated Circuit Design*

*Integrated Circuit Quality and Reliability*

**The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices. Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace, every effort has been made to present state-of-the-art material on all subjects covered in the book. This book is primarily designed as a comprehensive text for senior level and first-year graduate level digital circuit design classes, as well as a reference for practicing engineers in the areas of IC design and VLSI.**

**This introductory book assumes minimal knowledge of the existence of integrated circuits and of the terminal behavior of electronic components such as resistors, diodes, and MOS and bipolar transistors. It presents to readers the basic information necessary for more advanced processing and design books. Focuses mainly on the basic processes used in fabrication, including lithography, oxidation, diffusion, ion implementation, and thin film deposition. Covers interconnection technology, packaging, and yield. Appropriate for readers interested in the area of fabrication of solid state devices and integrated circuits.**

**There are many distinct pleasures associated with computer programming. Craftsmanship has its quiet rewards, the satisfaction that comes from building a useful object and making it work. Excitement arrives with the flash of insight that cracks a previously intractable problem. The spiritual quest for elegance can turn the hacker into an artist. There are pleasures in parsimony, in squeezing the last drop of performance out of clever algorithms and tight coding. The games, puzzles, and challenges of problems from international programming competitions are a great way to experience these pleasures while improving your algorithmic and coding skills. This book contains over 100 problems that have appeared in previous programming contests, along with discussions of the theory and ideas necessary to attack them. Instant online grading for all of these problems is available from two WWW robot judging sites. Combining this book with a judge gives an exciting new way to challenge and improve your programming skills. This book can be used for self-study, for teaching innovative courses in algorithms and programming, and in training for international competition. The problems in this book have been selected from over 1,000 programming problems at the Universidad de Valladolid online judge. The judge has ruled on well over one million submissions from 27,000 registered users around the world to date. We have taken only the best of the best, the most fun, exciting, and interesting problems available.**

**This book offers the first comprehensive view on integrated circuit and system design for the Internet of Things (IoT), and in particular for the tiny nodes at its edge. The authors provide a fresh perspective on how the IoT will evolve based on recent and foreseeable trends in the semiconductor industry, highlighting the key challenges, as well as the opportunities for circuit and system innovation to address them. This book describes what the IoT really means from the design point of view, and how the constraints imposed by applications translate into integrated circuit requirements and design guidelines. Chapter contributions equally come from industry and academia. After providing a system perspective on IoT nodes, this book focuses on state-of-the-art design techniques for IoT applications, encompassing the fundamental sub-systems encountered in Systems on Chip for IoT: ultra-low power digital architectures and circuits low- and zero-leakage memories (including emerging technologies) circuits for hardware security and authentication System on Chip design methodologies on-chip power management and energy harvesting ultra-low power analog interfaces and analog-digital conversion short-range radios miniaturized battery technologies packaging and assembly of IoT integrated systems (on silicon and non-silicon substrates). As a common thread, all chapters conclude with a prospective view on the foreseeable evolution of the related technologies for IoT. The concepts developed throughout the book are exemplified by two IoT node system demonstrations from industry. The unique balance between breadth and depth of this book: enables expert readers quickly to develop an understanding of the specific challenges and state-of-the-art solutions for IoT, as well as their evolution in the foreseeable future provides non-experts with a comprehensive introduction to integrated circuit design for IoT, and serves as an excellent starting point for further learning, thanks to the broad coverage of topics and selected references makes it very well suited for practicing engineers and scientists working in the hardware and chip design for IoT, and as textbook for senior undergraduate, graduate and postgraduate students (familiar with analog and digital circuits).**

**CMOS Logic Circuit Design**

**Basic Operational Amplifiers and Linear Integrated Circuits**

**Computer Architecture**

**Microelectronics**

**CMOS**

**Low-Energy FPGAs: Architecture and Design** is a primary resource for both researchers and practicing engineers in the field of digital circuit design. The book addresses the energy consumption of Field-Programmable Gate Arrays (FPGAs). FPGAs are becoming popular as embedded components in computing platforms. The programmability of the FPGA can be used to customize implementations of functions on an application basis. This leads to performance gains, and enables reuse of expensive silicon. Chapter 1 provides an overview of digital circuit design and FPGAs. Chapter 2 looks at the implication of deep-submicron technology on FPGA power dissipation. Chapter 3 describes the exploration environment to guide and evaluate design decisions. Chapter 4 discusses the architectural optimization process to evaluate the trade-offs between the flexibility of the architecture, and the effect on the performance metrics. Chapter 5 reviews different circuit techniques to reduce the performance overhead of some of the

dominant components. Chapter 6 shows methods to configure FPGAs to minimize the programming overhead. Chapter 7 addresses the physical realization of some of the critical components and the final implementation of a specific low-energy FPGA. Chapter 8 compares the prototype array to an equivalent commercial architecture.

This book offers comprehensive coverage of a wide, relevant array of operational amplifier topics. **KEY TOPICS:** The book integrates theory, practical circuits, and troubleshooting concepts, keeping mathematical details to a minimum. Delving more deeply into coverage of operational amplifiers, the book guides readers through a system of pedagogical tools that both reinforces and challenges their understanding. An essential reference in electronic technology.

**Computer Architecture: A Quantitative Approach, Sixth Edition** has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry

The first of two volumes in the **Electronic Design Automation for Integrated Circuits Handbook, Second Edition, Electronic Design Automation for IC System Design, Verification, and Testing** thoroughly examines system-level design, microarchitectural design, logic verification, and testing. Chapters contributed by leading experts authoritatively discuss processor modeling and design tools, using performance metrics to select microprocessor cores for integrated circuit (IC) designs, design and verification languages, digital simulation, hardware acceleration and emulation, and much more. New to This Edition: Major updates appearing in the initial phases of the design flow, where the level of abstraction keeps rising to support more functionality with lower non-recurring engineering (NRE) costs Significant revisions reflected in the final phases of the design flow, where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting-edge applications and approaches realized in the decade since publication of the previous edition—these are illustrated by new chapters on high-level synthesis, system-on-chip (SoC) block-based design, and back-annotating system-level models Offering improved depth and modernity, **Electronic Design Automation for IC System Design, Verification, and Testing** provides a valuable, state-of-the-art reference for electronic design automation (EDA) students, researchers, and professionals.

**Analysis and Design, Second Edition**

**Analog Circuit Design**

**A Quantitative Approach**

**Digital Integrated Circuit Design**

**Brutal**

*The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic, instead of electronics, level. It covers a range of topics, from number system theory to asynchronous logic design. A solution manual is available to instructors only.*

*Requests must be made on official school stationery.*

*The striking feature of this book is its coverage of the upper GHz domain. However, the latest technologies, applications and broad range of circuits are discussed. Design examples are provided including cookbook-like optimization strategies. This state-of-the-art book is valuable for researchers as well as for engineers in industry. Furthermore, the book serves as fruitful basis for lectures in the area of IC design.*

*Introduction to Microelectronic Fabrication*

*Operational Amplifiers, Analog to Digital Convertors, Analog Computer Aided Design*

*Low-Energy FPGAs — Architecture and Design*

*A Guide to Digital Design and Synthesis*

*Radio Frequency Integrated Circuits and Technologies*